



PATENT

Attorney Docket No. 9052-162

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Farnsworth

U.S. Patent No.: 7,168,024 B2

Application No.: 10/678,625

Issued: January 23, 2007

Filed: October 3, 2003

For: DATA PROCESSING SYSTEM AND METHOD

February 23, 2007

Commissioner for Patents

Attn: Certificate of Correction Branch

P.O. Box 1450

Alexandria, VA 22313-1450

**REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION UNDER
35 U.S.C §254 AND 37 C.F.R. §1.322**

Sir:

The Assignee of record for the above-referenced patent hereby requests, pursuant to 35 U.S.C §254 and 37 C.F.R. §1.322, that a Certificate of Correction be issued. This request is made in order to correct the mistakes incurred through the fault of the U.S. Patent and Trademark Office. No fee is believed due. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

The mistakes appearing in the patent are set forth with corrections on the Certificate of Correction enclosed herewith, with an additional copy thereof and a return post card.

Respectfully submitted,

D. Scott Moore

Registration No. 42,011

**Certificate
MAR 02 2007
of Correction**

Myers Bigel Sibley & Sajovec, P.A.
P. O. Box 37428, Raleigh, NC 27627
Telephone: (919) 854-1400
Facsimile: (919) 854-1401
Customer No. 20792

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Attn: Certificate of Correction Branch, P.O. Box 1450, Alexandria, VA 22313-1450, on February 23, 2007.

Jessica M. French

MAR - 9 2007

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,168,024 B2
DATED : January 23, 2007
INVENTOR(S) : Farnsworth

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On The Title Page,

Sec. [57]: ABSTRACT should read: --A method for generating at least one error checking code includes creating a modified input data word, including (k+m) bits, by combining an input data word, having n-bits, with at least a previously calculated at least one error checking code, including m-bits, where $n \leq k$, and calculating a first m-bit error checking code from at least a first selected portion of the modified input data word. --

Column 13,

Line 10: Should read: --code, comprising m-bits, where $n \leq k$, and --

MAILING ADDRESS OF SENDER:
Myers, Bigel, Sibley & Sajovec
P.O. Box 37428
Raleigh, NC 27627

PATENT NO. 7,168,024 B2

No. of additional copies: 1



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

9 2007

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,168,024 B2
DATED : January 23, 2007
INVENTOR(S) : Farnsworth

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On The Title Page,

Sec. [57]: ABSTRACT should read: --A method for generating at least one error checking code includes creating a modified input data word, including $(k+m)$ bits, by combining an input data word, having n -bits, with at least a previously calculated at least one error checking code, including m -bits, where $n \leq k$, and calculating a first m -bit error checking code from at least a first selected portion of the modified input data word. --

Column 13,

Line 10: Should read: --code, comprising m -bits, where $n \leq k$, and --

MAILING ADDRESS OF SENDER:
Myers, Bigel, Sibley & Sajovec
P.O. Box 37428
Raleigh, NC 27627

PATENT NO. 7,168,024 B2
No. of additional copies: 1



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.